BR460Pro V1.2.0.0 Program Release Notes

Release

1. 2025.11.10 V1.2.0.0

Features

1. Maximum load capacity: 512×512 pixels.

2. Up to 1/64 scan.

3. Up to 40 groups of parallel data; 64 groups of serial data (expandable to 128 groups of serial data). Note: This refers to physical load.

4. Image booster 2.1.

5. Color management.

6. 22bit+.

7. Full-grayscale 2.0.

8. Pixel level brightness and chroma calibration.

9. Quick seam correction. Note: Performing without input source is supported and independent on/off switch is also supported.

10. Seam correction with cameras.

11. Low latency. Note: Low latency is enabled by default, and non-low latency is not supported.

12. 3D. Note: The load capacity will be reduced by half.

13. Individual gamma adjustment for RGB.

14. Image rotation in 90° increments.

15. Free image rotation.

16. HDR function.

17. Display of 3-color 16-point serial input.

18. Smart module (dedicated firmware required).

19. Automatic module calibration.

20. Module flash management.

21. One click to apply calibration coefficients in module flash.

22. Mapping function.

23. Settings of a stored image in the receiving card.

24. Temperature and voltage monitoring.

25. Cabinet LCD. Note: Only 5-Pin LCD is supported.

26. Bit error detection.

27. Status detection of dual power supplies.

28. Firmware program readback.

29. Configuration parameter readback.

30. LVDS transmission (dedicated firmware required).

31. Dual card backup and status monitoring.

32. Loop backup.

33. Dual backup of configuration parameters.

34. Dual program backup.

35. No rectangle restriction. Note: Requires working with all-in-one products.

36. Adjustable EMC. Note: Requires NovaLCT 5.4.8 or later.

37. Input source supports 10-bit/12-bit. Note: With an M3 sending card, 10-bit/12-bit sources support a load capacity of 512×384 pixels. With a COEX sending card, 10-bit sources support 512×512 pixels, while 12-bit sources support 512×384 pixels.

38. Thermal Compensation 2.0.

39. NCP backup and restoration.

40. Data group mapping.

41. Multi-mode.

42. Bit error detection 2.0.

43. Energy saving mode.

44. Cabinet painter.

Others

1. No additional notes listed.

Driver IC

1. MBI series: MBI502x, MBI503x, MBI5040, MBI5041(B), MBI5042(B), MBI5043, MBI505x, MBI5124 (excluding MBI5124DPWM), MBI5125 (excluding MBI5125DPWM), MBI515x, MBI5252, MBI5353, and MBI5153B.

2. SUM series: SUM20167, SUM2017(T), SUM2028, SUM203x, SUM213x.

3. MY series: MY9266, MY9269, MY9366, MY9862, MY9868.

4. ICN series: ICN2027, ICN2028, ICN2038, ICN2038S, ICN2053, ICN2055, ICN2063.

5. SM series: SM16158, SM16159, SM16207, SM16227, SM16237, SM16259.

6. Others: TLC5958, TLC59581, SCL8060, common chips.

Decoding Chip

1. 74HC138, 74HC595, RT5953, RT5958.

2. SM5266, ICN2012/2013.

3. ICN2018/2019.